



## BMR 351XX02/803

### 1 600W digital quarter brick DC/DC IBC

The BMR351XX02/803 is a high-power digital DC/DC converter. The impressive performance of this converter includes an efficiency reaching 97.8% at 54V<sub>in</sub> at half load. The BMR351XX02/803 is a non-isolated quarter brick. The converter delivers a fully regulated 12.50V, with a continuous power level of 1600W and has a peak power capability of up to 2320W for limited time.

This converter is designed for through-hole mounting using wave solder or pin-in-paste production, and incorporates a novel design of baseplate, which optimizes thermal performance while minimizing height.



### Key features

- High efficiency with 97.8%
- Non-isolated
- 12.50 V fully regulated
- Event data recorder (black-box)
- Parallel operation with droop load share
- Screwed baseplate
- Monotonic start-up
- Output over voltage protection
- Over temperature protection
- Output short-circuit protection
- Remote control
- PMBus configuration

### Soldering methods

- Reflow soldering pin-in-paste
- Wave soldering
- Manual soldering

### Key electrical information

Parameter	Values
Input range	40-60 V
Output voltage	12.50 V
Output current	136 A
Peak current	200 A
Output power	1600 W
Peak power	2320 W

### Mechanical

58.4 x 36.8 x 14.7 mm / 2.30 x 1.45 x 0.58 in

### Application areas

- Datacom applications

## Product options

The table below describes the different product options.

Example:	BMR351	2	2	02	/803	H	Definitions
<b>Product family</b>	BMR351						
<b>Pin length options</b>		2					0 = 5.33 mm / 0.21 in 2 = 3.69 mm / 0.15 in 3 = 4.57 mm / 0.18 in 4 = 2.79 mm / 0.11 in
<b>Baseplate / HS option</b>			2				0 = Open frame, 14.2 mm / 0.559 in height 2 = Baseplate closed deck, 14.7 mm / 0.58 in height
<b>Other hardware options</b>				02			02 = 7-pin digital header, Sense $\pm$
<b>Configuration code</b>					/803		/803 = 12.50V <sub>out</sub> Optimized DLS configuration for 40-60V <sub>in</sub>
<b>Packaging options</b>						H	E = soft tray, dry pack (PIP reflow soldering) H* = hard tray, dry pack (PIP reflow soldering) blank = foam tray (no dry pack, wave soldering) * hard tray only available for baseplate version

For more information, please refer to Part 3 [Mechanical information](#).

If you do not find the variant you are looking for, please contact us at [Flex Power Modules](#).

## Order number examples

Part number	V <sub>in</sub>	Outputs	Configuration
BMR3512202/803H	40-60V	12.50V / 136A / 1600W	3.69 mm pins / 7-pin digital header, Sense $\pm$ / base late closed deck/ DLS / dry pack, hard tray

## Part 1: Electrical specifications

### Absolute maximum ratings

Stress in excess of our defined *absolute maximum ratings* may cause permanent damage to the converter. Absolute maximum ratings, also referred to as *non-destructive limits*, are normally tested with one parameter at a time exceeding the limits in the electrical specification.

Characteristics	min	typ	max	Unit
Operating temperature ( $T_{P1}$ )	-40		+125	°C
Storage temperature	-55		+125	°C
Input voltage ( $V_{in}$ )	-0.5		+65	V
Input voltage transient (100 ms)			+80	V
Isolation voltage (input to output)			0	V
Isolation voltage (baseplate to output)			0	V
Remote control pin voltage* (Note 1)	-0.3		5	V

\*Note 1: measurement at 20MHz bandwidth

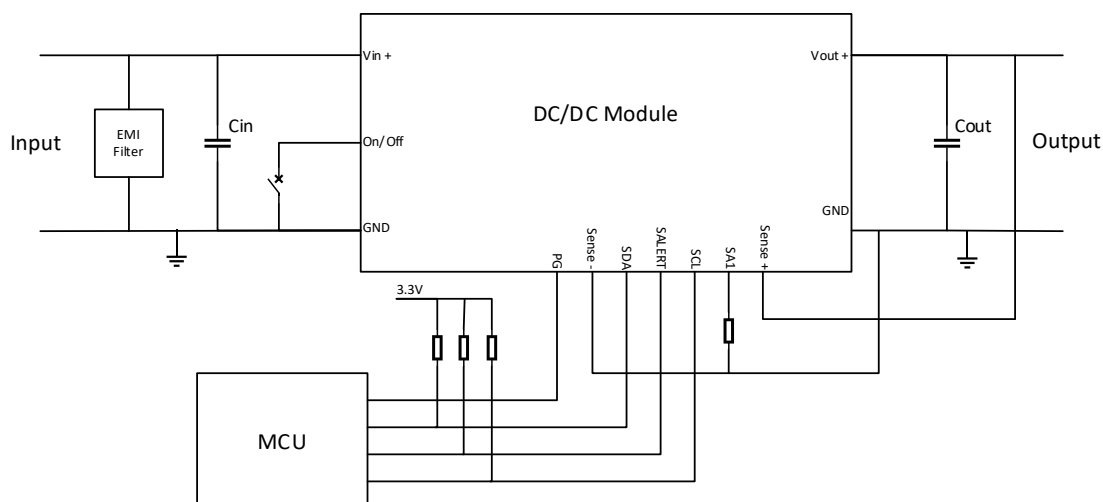
### Reliability

Failure rate ( $\lambda$ ) and mean time between failures ( $MTBF = 1/\lambda$ ) are calculated based on *Telcordia SR-332 Issue 4: Method 1, Case 3, (80% of  $I_{out\_TDP}$ ,  $T_{P1}=40^{\circ}C$ , Airflow=200 LFM)*.

	Mean	90% confidence level	Unit
Steady-state failure rate ( $\lambda$ )	131	159	nfailures/h
Standard deviation ( $\sigma$ )	22.4		nfailures/h
MTBF	7.65	6.28	MHr

### Typical application diagram

Capacitor values are defined in the Electrical Specification tables. The EMI filter is defined in the [EMC Part 2](#).



## Part 1: Electrical specifications

**Electrical specifications for BMR351 XX02/803****12.50V, 136A (200A) ≤ 1600W (2320W)**

Min and Max values are valid for:  $T_{P1} = -30$  to  $+90^{\circ}\text{C}$ ,  $V_{in} = 40\text{V}$  to  $60\text{V}$ , unless otherwise specified under conditions. Typical values given at:  $T_{P1} = +25^{\circ}\text{C}$ ,  $V_{in} = 54\text{V}$ , max  $P_{out\_TDP}$ , unless otherwise specified under conditions, see Note 1.

Additional external  $C_{in} = 220\mu\text{F}$ ,  $C_{out} = 2\text{mF}$

Characteristic	conditions	minimum	typical	maximum	unit
<b>Key features</b>					
Efficiency ( $\eta$ )	50% of $P_{out\_TDP}$		97.8		%
	100% of $P_{out\_TDP}$		96.9		%
	50% of $P_{out\_TDP}$ $V_{in} = 48\text{V}$		97.9		%
	100% of $P_{out\_TDP}$ $V_{in} = 48\text{V}$		96.9		%
$P_{out\_TDP}$ thermal design power (TDP)	See Note 1			1600	W
$P_{out\_MAX}$ peak power ( $t \leq 0,5\text{s}$ )	See Note 1			2320	W
Power dissipation	100% of $P_{out\_TDP}$		51	70	W
Switching frequency ( $f_s$ )	0-100 % of $P_{out\_TDP}$		150		kHz
Recommend capacitive load	See Note 2	2000		20000	$\mu\text{F}$
<b>Input characteristics</b>					
Input voltage range ( $V_{in}$ )		40		60	V
Input idling power	$P_{out} = 0\text{W}$		5.5	13	W
Input standby power	(turned off with RC)		552	768	mW
Input OVP			85		V
Internal input capacitance			90		$\mu\text{F}$
Recommended external input capacitance	See Note 3	220			$\mu\text{F}$

Note 1: Max. output current is rated at 136A. Max continuous power (thermal design power (TDP) is  $\leq 1600\text{W}$  depending on thermal conditions.

Note 2: Minimum 2000 $\mu\text{F}$  OS-CON capacitor.

Note 3: Recommended to use 2x100 $\mu\text{F}$  electrolyte capacitor + 1x27 $\mu\text{F}$  OS-CON capacitor.

**Electrical specifications for BMR351 XX02/803****12.50V, 136A (200A) ≤ 1600W (2320W)**

Min and Max values are valid for:  $T_{P1} = -30$  to  $+90^{\circ}\text{C}$ ,  $V_{in} = 40\text{V}$  to  $60\text{V}$ , unless otherwise specified under conditions. Typical values given at:  $T_{P1} = +25^{\circ}\text{C}$ ,  $V_{in} = 54\text{V}$ , max  $P_{out\_TDP}$ , unless otherwise specified under conditions, see Note 1.

Additional external  $C_{in} = 220\mu\text{F}$ ,  $C_{out} = 2\text{mF}$

Characteristic	conditions	minimum	typical	maximum	unit
<b>Output characteristics</b>					
Output voltage initial setting and accuracy	$P_{out} = 0\text{ W}$	12.47	12.50	12.53	V
Output voltage tolerance band	0 – 100% of max $P_{out\_TDP}$ $V_{in} = 40\text{-}60\text{ V}$	11.4		12.7	V
Output adjust range	0-100% of max $P_{out\_TDP}$	8		13.2	V
Idling voltage	$P_{out} = 0\text{ W}$ , 54 V	12.27		12.69	V
Line regulation	$V_{in} = 40 - 60\text{ V}$ 0 – 100% of max $P_{out\_TDP}$		5	70	mV
Load regulation	0 - 100% of max $P_{out\_TDP}$		800	880	mV
Output current ( $I_{out}$ )	$V_{in} = 40 - 60\text{ V}$	0		136	A
Load transient voltage deviation	Load step 25-75-25% of max $P_{out\_TDP}$ $di/dt = 2.5\text{ A}/\mu\text{s}$ . See Note 2		±440	±800	mV
Load transient recovery time			100		μs
Output ripple & noise	max $P_{out\_TDP}$ See Note 3		60	120	mV <sub>p-p</sub>

Note 1: Max. output current is rated at 136A. Max continuous power (thermal design power (TDP)) is  $\leq 1600\text{W}$  depending on thermal conditions.

Note 2:  $C_{out}$  is 2 x 1mF

Note 3: See Technical Reference doc: Design considerations

**Electrical specifications for BMR351 XX02/803****12.50V, 136A (200A) ≤ 1600W (2320W)**

Characteristic	conditions	minimum	typical	maximum	unit
<b>On/off control</b>					
Turn-off input voltage	Decreasing input voltage	34	35	36	V
Turn-on input voltage	Increasing input voltage	36	37	38	V
Ramp-up time (from 0–100% of V <sub>out</sub> )		7	10	13	ms
Start-up time (from V <sub>in</sub> connection to 90% of V <sub>out</sub> )			40		ms
RC start-up time			25		ms
Logic high: trigger level			1.4		V
Logic low: trigger level			1.3		V
Logic low: response time		0.1	0.2	0.3	ms
Sink current		0.4			mA
<b>Protection features</b>					
Current limit threshold (OCP)	T <sub>P1</sub> < max T <sub>P1</sub>	200	220	240	A
Output current limit (OCP) response time and type	Disable and do not retry		35		μs
Current limit threshold (Slow OCP)			163		A
Current limit Off Delay (Slow OCP)	Load step 136-170A. See note 1			200	ms
	Load step 136-200A. See note 1			70	ms
Output overvoltage protection (OVP)			14.2		V
Output overvoltage protection (OVP) response time and type	Disabled until fault cleared		70		μs
Over temperature protection (OTP)	See note 2		125		°C
Over temperature protection (OTP) re-start temperature and type	Disabled until fault cleared		90		°C

Note 1: Off Delay is dependent on averaging power level above TDP, 136A. Product is configured to allow 200A/2320W for max 200ms. High di/dt on load step up to peak power, might cause current overshoot resulting in OCP fault. See Technical Reference Document Design & Application Guidelines for detailed information.

Note 2: Please attach thermocouple on NTC resistor to test OTP function, the hot spot (P1) temperature is just for reference.

**Electrical specifications for BMR351 XX02/803****12.50V, 136A (200A) ≤ 1600W (2320W)**

In the table below all PMBus are written in capital letters.

$T_{P1} = -30$  to  $+90^{\circ}\text{C}$ ,  $V_{in} = 40\text{V}$  to  $60\text{V}$ , unless otherwise specified under conditions.

Typical values given at:  $T_{P1} = +25^{\circ}\text{C}$ ,  $V_{in} = 54\text{V}$ , max  $P_{out\_TDP}$ , unless otherwise specified under conditions

Command	Conditions	minimum	typical	maximum	Unit
<b>Monitoring accuracy</b>					
Input voltage READ_VIN			±125		mV
Output voltage READ_VOUT			±40		mV
Output current READ_IOUT	$T_{P1} = 25^{\circ}\text{C}$ , $V_O = 12\text{V}$		±1		A
	$T_{P1} = -20-120^{\circ}\text{C}$ , $V_O = 12\text{V}$		±5		A
Duty cycle READ_DUTY_CYCLE	No tolerance, Read value is the actual value applied by PWM controller				
Temperature READ_TEMPERATURE_1	Temperature sensor, $-20-120^{\circ}\text{C}$		±5		$^{\circ}\text{C}$

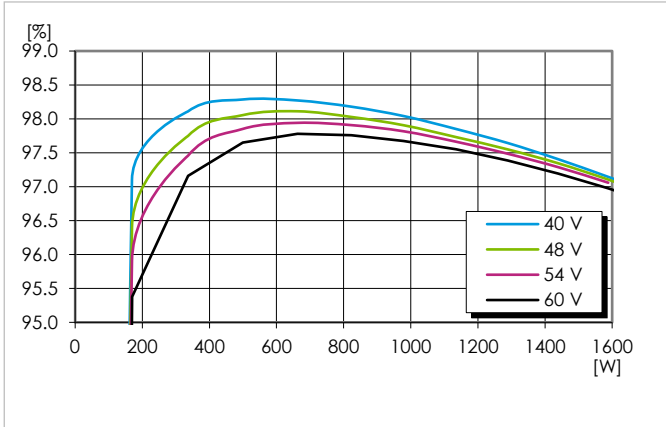
For more detailed information please refer to Technical Reference Document: PMBus commands.

This product is supported by the [Flex Power Designer tool](#).

### Electrical graphs for BMR351XX02/803

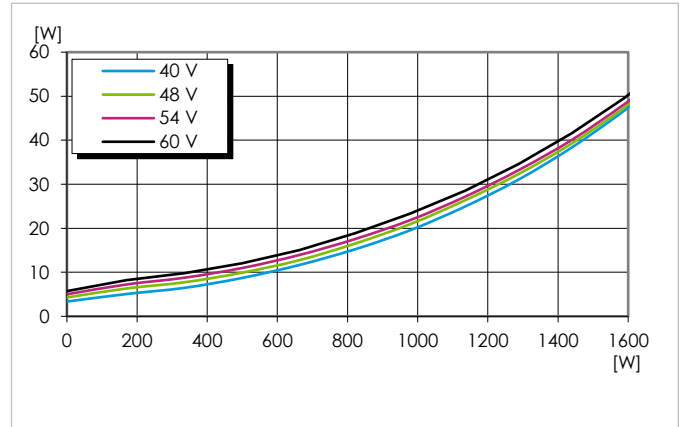
12.50V, 136A (200A) ≤ 1600W (2320W)

#### Efficiency



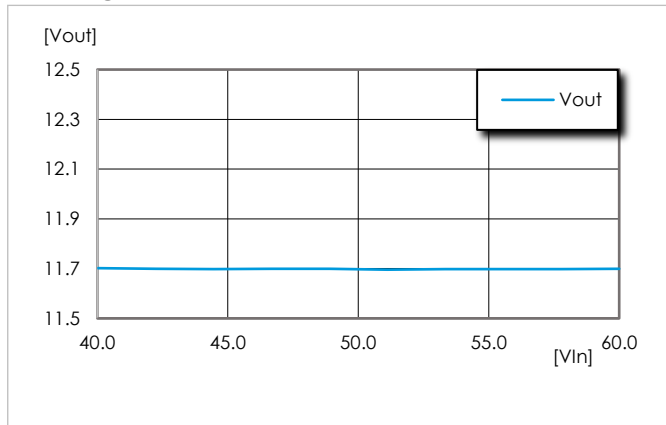
Efficiency vs. output power and input voltage at  $T_{PI} = +25^{\circ}\text{C}$

#### Power dissipation



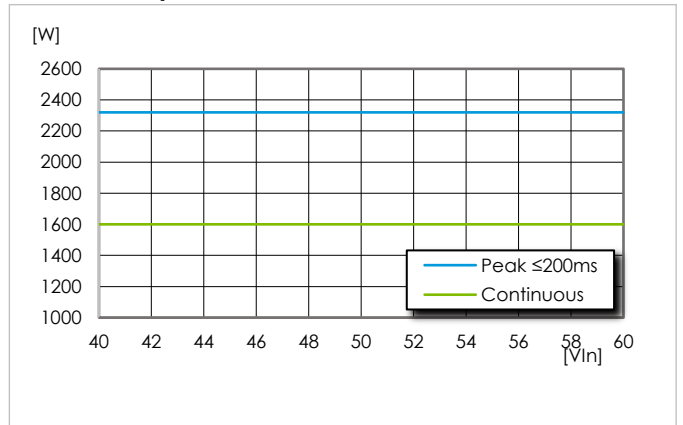
Dissipated power vs. output power at  $T_{PI} = +25^{\circ}\text{C}$

#### Line regulation



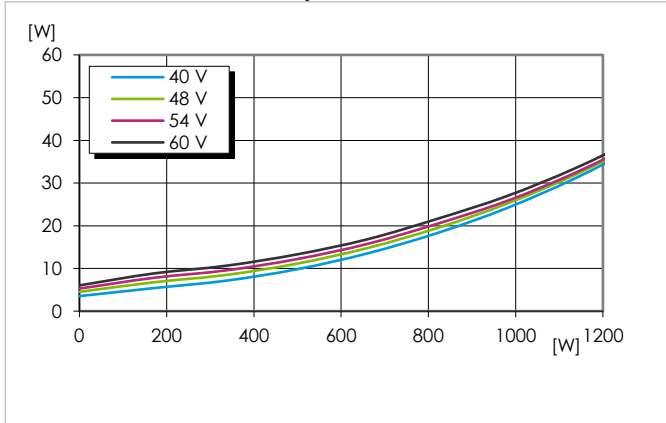
Output voltage vs. input voltage at  $T_{PI} = +90^{\circ}\text{C}$ , 100% of max  $P_{out\_TDP}$

#### Available power



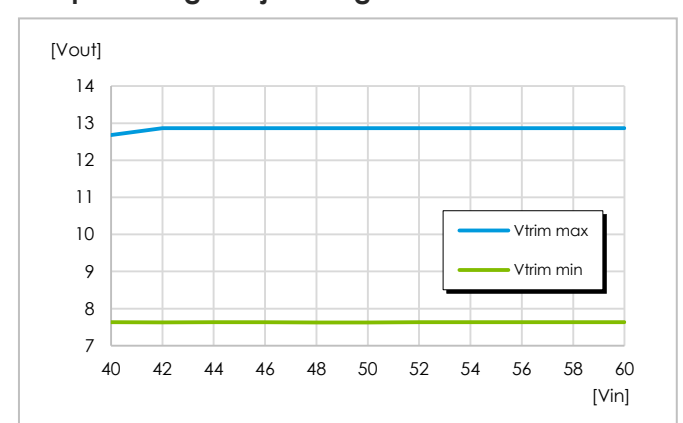
Available output power vs. input voltage,  $T_{PI} = +90^{\circ}\text{C}$

#### Power loss at max temperature



Dissipated power vs. output power and input voltage at  $T_{PI} = +90^{\circ}\text{C}$ . Above 1600W only peak  $t \leq 0.5\text{s}$

#### Output voltage adjust range

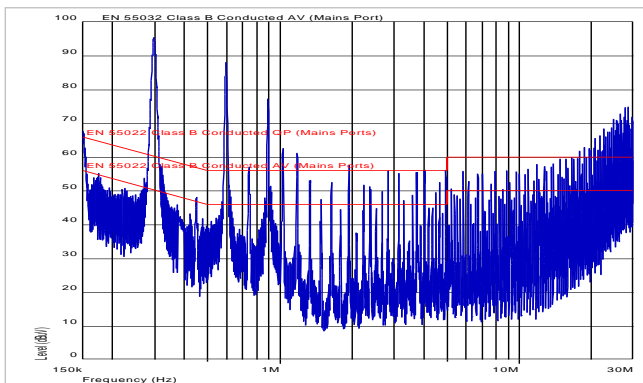


Max and min  $V_{out}$  trim vs  $V_{in}$ . At  $P_{out\_TDP}$ , 1600W

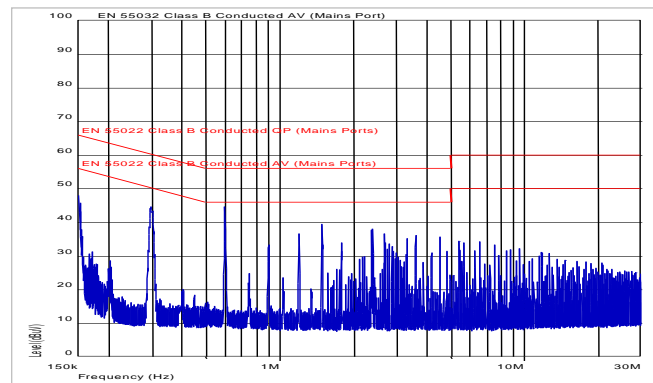
## Part 2: EMC

### EMC specifications

Conducted EMI measured according to EN55022 / EN55032, CISPR 22 / CISPR 32 and FCC part 15J (see test set-up below). The fundamental switching frequency is 150kHz for BMR351. The EMI characteristics below is measured at  $V_{in} = 54V$  and max  $I_{out}$ .



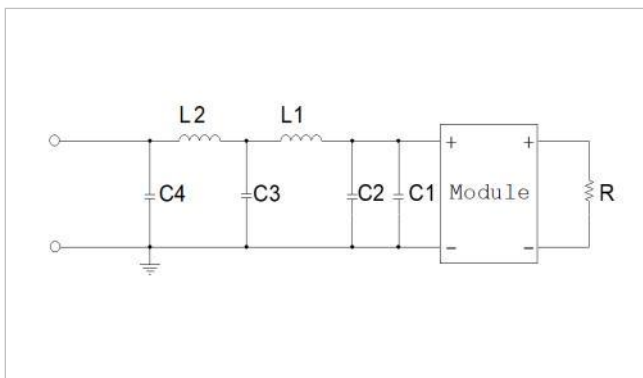
EMI without filter. EN55032 test method and limits are the same as EN55022. 1000 $\mu$ F + 470 $\mu$ F 100V input capacitor and 4000 $\mu$ F 16V OS-CON output capacitor used



EMI with filter, EN55032 test methods and limits are the same as EN55022

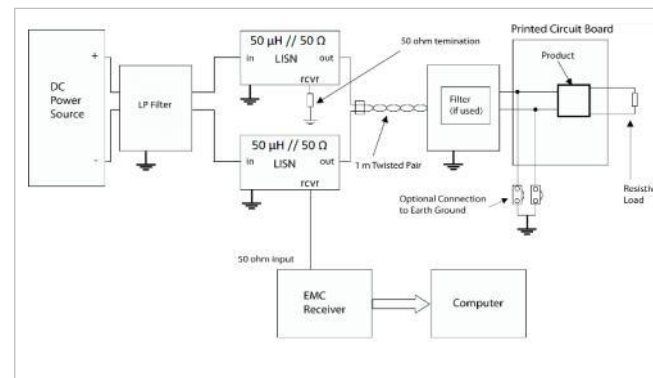
### Optional external filter for Class B

Suggested external input filter in order to meet Class B in EN 55022 / EN 55032, CISPR 22 / CISPR 32 and FCC part 15J.



#### Filter components:

- C1 = 1000 $\mu$ F + 470 $\mu$ F (electrolyte)
- C2 = 2 x 2.2 $\mu$ F + 10 $\mu$ F
- C3 = 2 x 10 $\mu$ F
- C4 = 10 $\mu$ F
- L1 = 2.2 $\mu$ H
- L2 = 2.2 $\mu$ H



Test set-up

### Layout recommendations

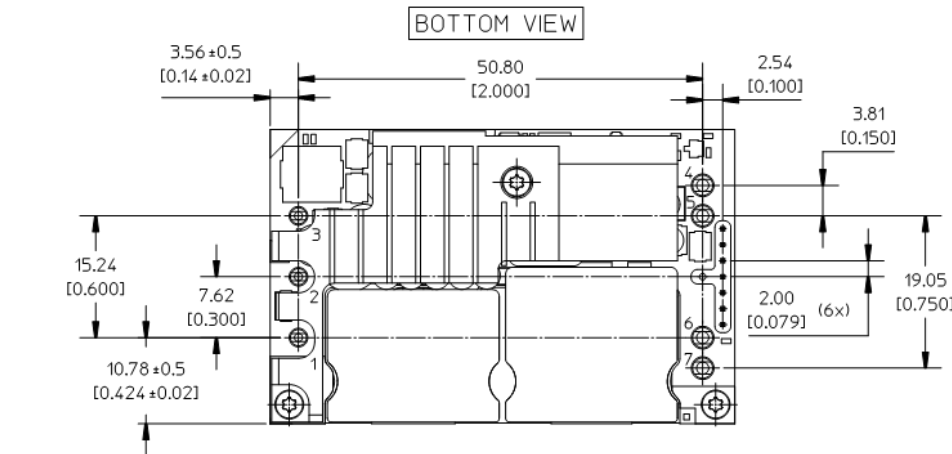
The radiated EMI performance of the product will depend on the PWB layout and ground layer design. It is also important to consider the stand-off of the product. If a ground layer is used, it should be connected to the output of the product and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PWB and improve the high frequency EMC performance.

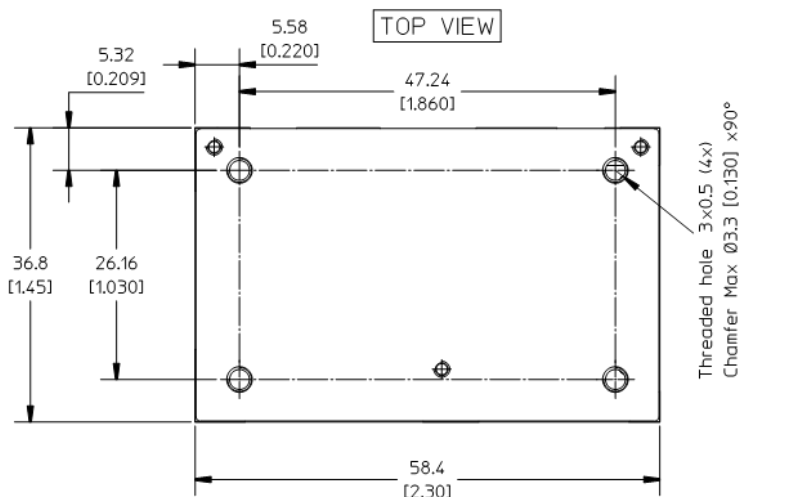
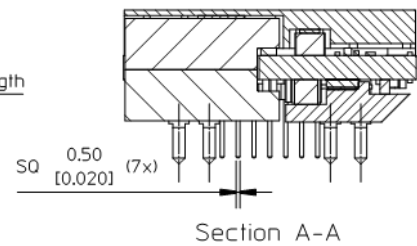
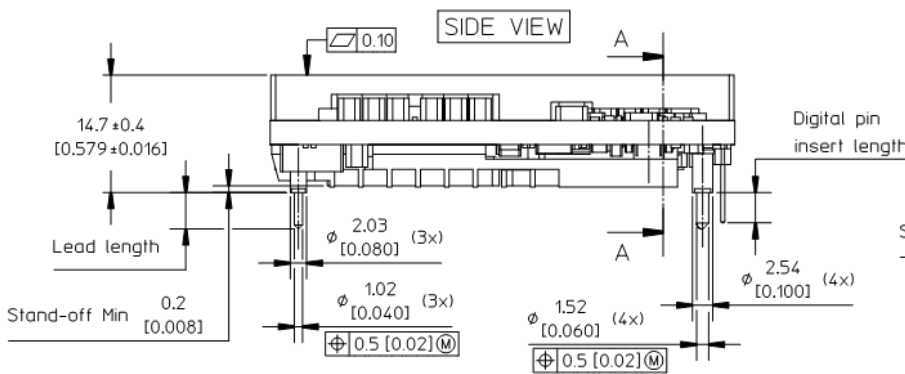
### Part 3: Mechanical information

#### BMR351X2XX/XXX: hole mounted, baseplate version

The mechanical information is based on a module which is hole mounted and has a baseplate.



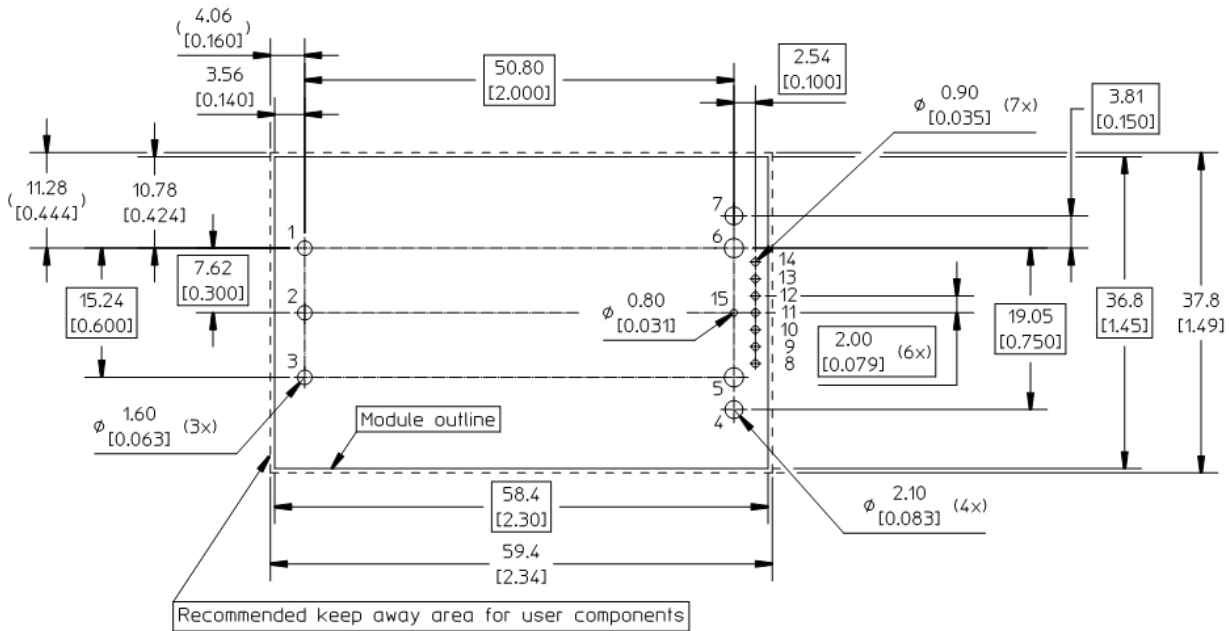
	Lead length	Digital pin insert length
Standard	5.33 [0.210]	-
LA	3.69 [0.145]	2.93 [0.115]
LB	4.57 [0.180]	3.80 [0.150]
LC	2.79 [0.110]	2.79 [0.110]



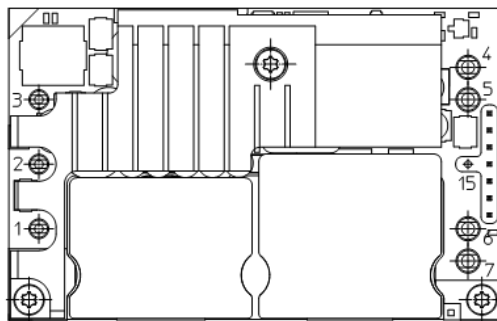
**NOTES:**  
**BASEPLATE INTERFACE**  
 Material: Aluminium  
 For screw attachment apply mounting torque of max 0.48 Nm [4.3 lbf in.]. M3 screw must not protrude more than 4.0 mm [0.157 in.] into the baseplate.  
**WEIGHT**  
 Typical: 110 g  
 All dimensions are in mm and [inch]. Tolerances unless specified:  
 x.x mm ±0.50 mm, [x.xx in. ±0.02 in.]  
 x.xx mm ±0.25 mm, [x.xxx in. ±0.010 in.]  
**3D MODELS**  
 3D .stp models are available on request.  
 All component placements, whether shown as physical components or symbolical outline, are for reference only and are subject to change throughout the product's life cycle unless explicitly described and dimensioned in this drawing.

Footprint and pin positions - BMR351X2XX/XXX: hole mounted, baseplate version

OUTLINE AND RECOMMENDED FOOTPRINT - TOP VIEW



CONNECTIONS - BOTTOM VIEW



**PIN SPECIFICATIONS:**

Pin 1-3, 4-7 Material: Copper alloy  
Plating: Min Au 0.1 µm over Ni 1-3 µm

Pin 8-14 Material: Brass  
Plating: Min Au 0.1 µm over Ni 1-3 µm

Pin 15 Not mounted (Option)

Pin	Designation	Function PCB pins
1	+In	Positive Input
2	RC	Remote Control
3	-In	Negative Input
4	-Out	Negative Output
5	-Out	Negative Output
6	+Out	Positive Output
7	+Out	Positive Output

Pin	Designation	Function 7 pin Connector
8	PG	Power Good
9	SENSE -	Remote Sense -
10	SDA	PMBus Data
11	SALERT	PMBus alert signal
12	SCL	PMBus Clock
13	SA1	PMBus Address 1
14	SENSE +	Remote Sense +

## Part 4: Thermal considerations

### Thermal considerations

The products are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.

#### General

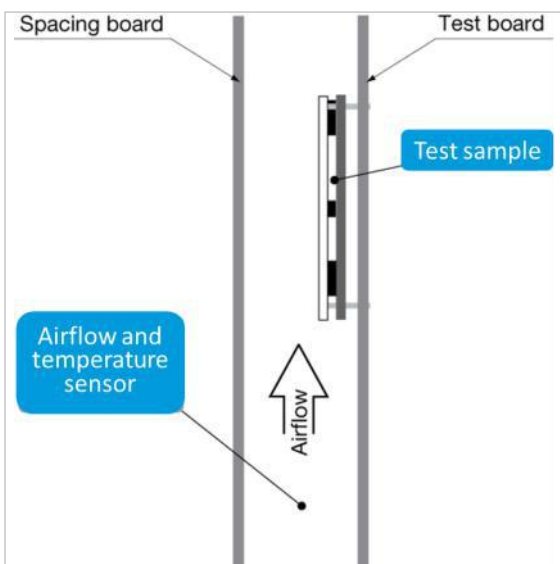
For products mounted on a PWB without a heatsink attached, cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependent on the airflow across the product. Increased airflow enhances the cooling of the product. The wind speed and temperature are measured in a point upstream the device. The *output current derating graphs* found later in this section for each model provide the available output current vs. ambient air temperature and air velocity at  $V_{in} = 54\text{ V}$ .

For products using any form of heatsink structure a top spacing board and side airflow guides are used to ensure airflow hitting the module and not diverted away.

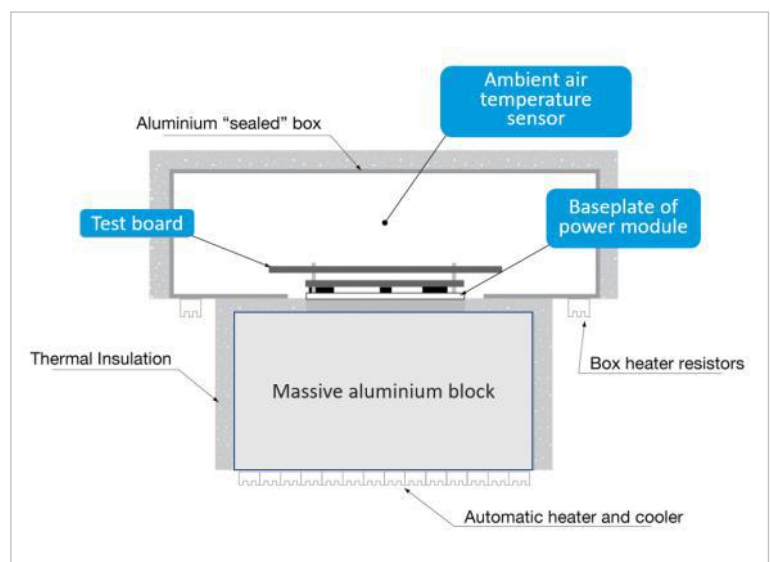
Distance between the tested device and the top space board and the side airflow guides are  $6.35\text{ mm} \pm 1\text{ mm}$ .

The product is tested on a  $185 \times 185\text{ mm}$ ,  $105\text{ }\mu\text{m}$  (3 oz), 6-layer test board mounted vertically in a wind tunnel.

For products with baseplate used in a sealed box/cold wall application, cooling is achieved mainly by conduction through the cold wall. The product is tested in a sealed box test set up with ambient temperatures  $85^\circ\text{C}$ . See [Design Note 028](#) for further details.



Picture: general test set-up



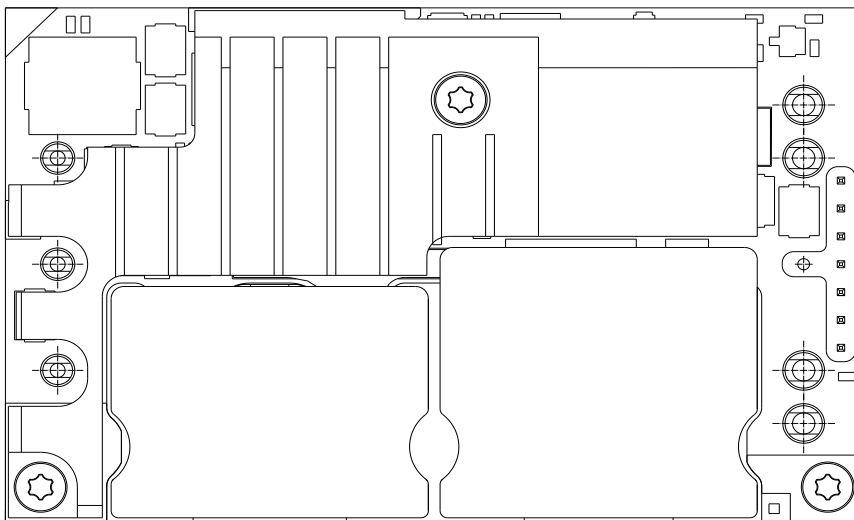
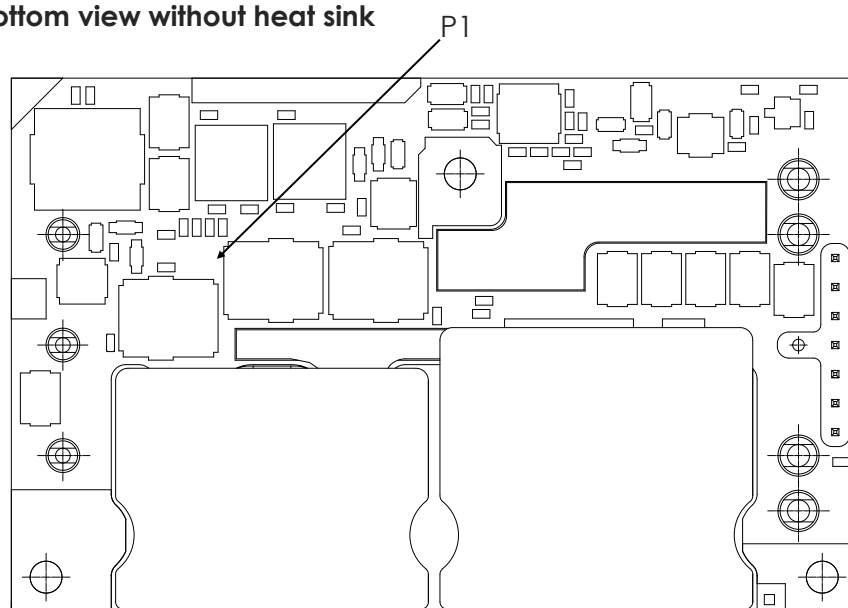
Picture: cold wall test set-up

## Part 4: Thermal considerations

**Definition of product operating temperature**

Proper thermal conditions can be verified by measuring the temperature at position P1 as shown below. The temperature at this position ( $T_{P1}$ ) should not exceed the maximum temperatures in the table below. The number of measurement points may vary with different thermal design and topology. Temperatures above maximum  $T_{P1}$ , measured at the reference point P1 are not allowed and may cause permanent damage. See Note 1.

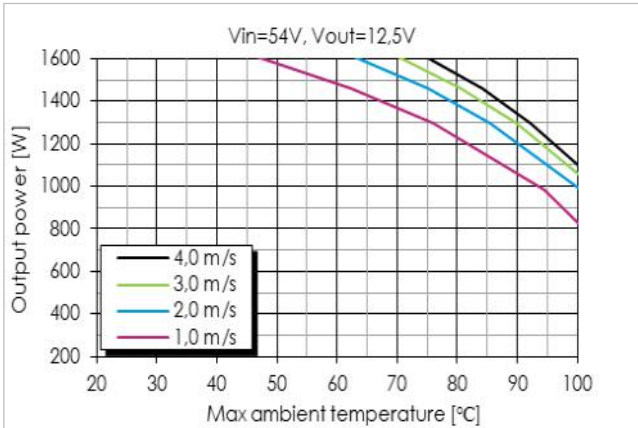
Position	Description	Max. Temp.
P1	PWB reference point	$T_{P1} = 125^{\circ}\text{C}$

**Bottom view with heat sink****Bottom view without heat sink**

Note 1: Heat sink needs to be unscrewed in order to attach thermal probe to the component lead.

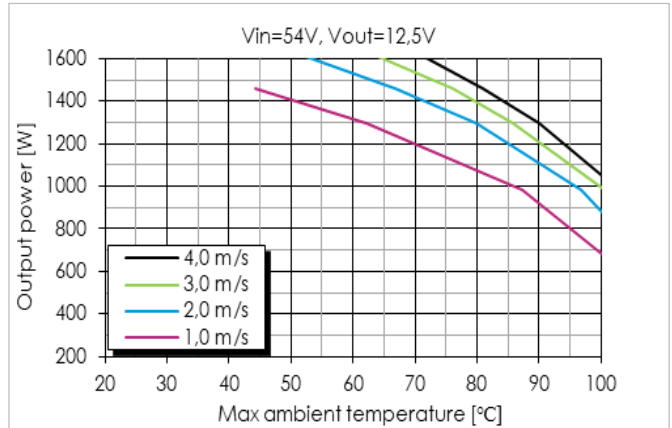
## Thermal graphs

### Output power derating - 1.0 inch heatsink



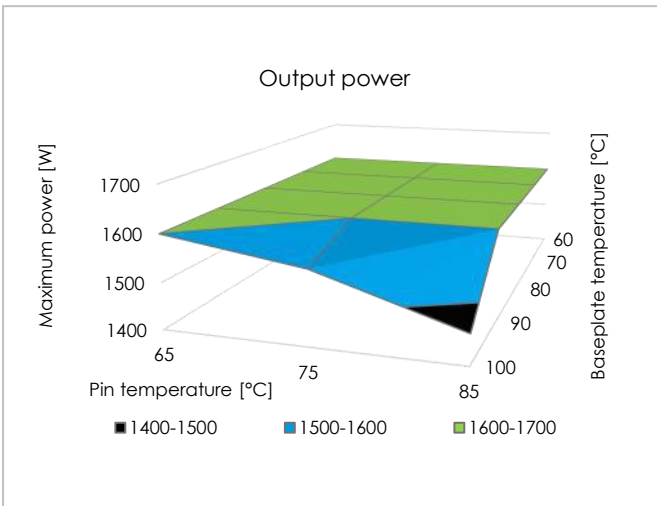
Available output power vs. ambient air temperature and airflow.  
Airflow Direction -IN to +IN.

### Output power derating - 0.5 inch heatsink



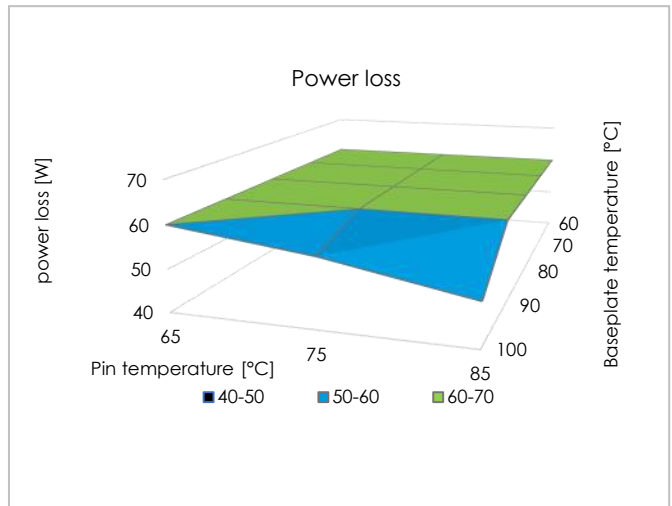
Available output power vs. ambient air temperature and airflow.  
Airflow Direction -IN to +IN.

### Maximum Output Power



Maximum allowed thermal power (restriction by POMAX might occur).

### Maximum Allowed Power Loss



Available output power vs. pin and baseplate temperature. See Thermal Consideration section.

For more information, please refer to our [thermal models](#) on the website.

## Part 5: Packaging

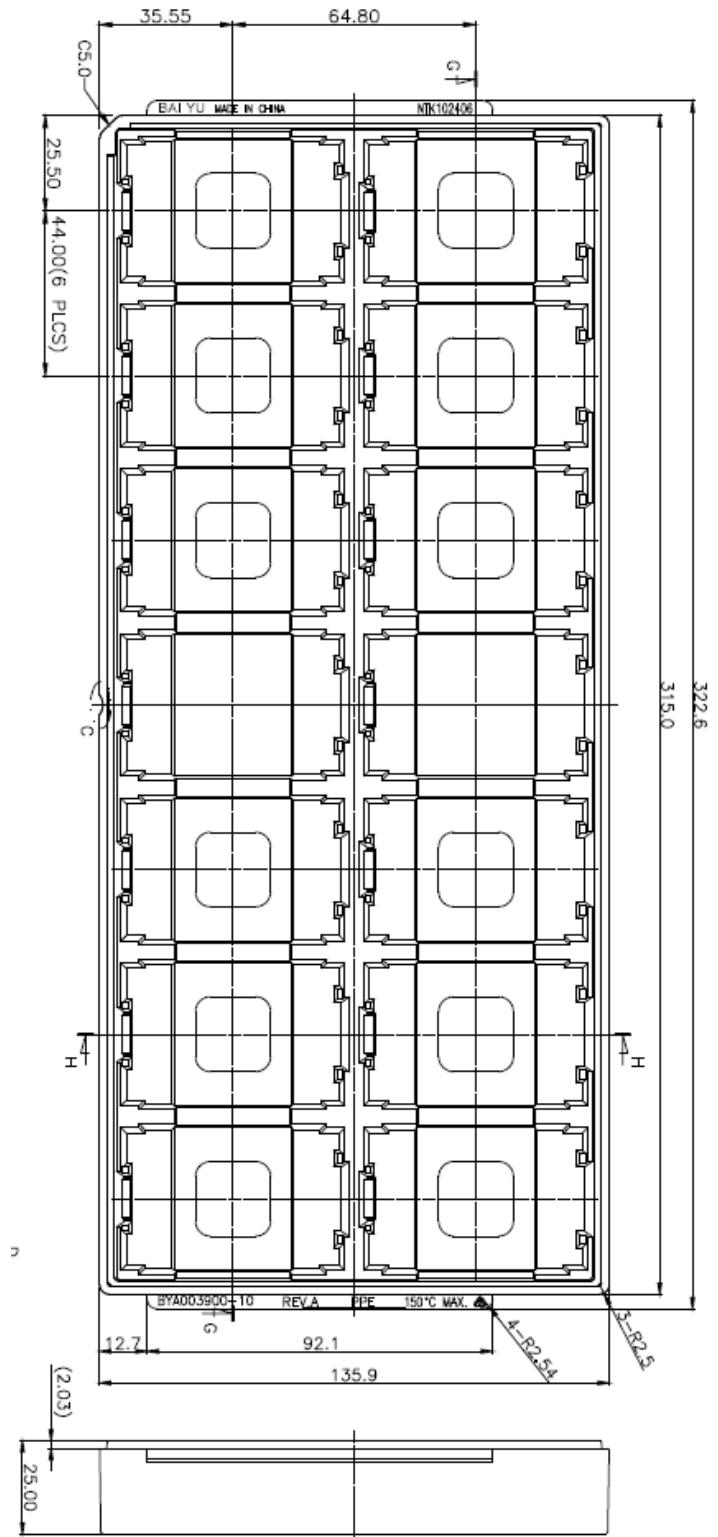
### Packaging information

**H option:** Select for PIP reflow solder and pick & place - dry packed (Baseplate version)

<b>Material</b>	Antistatic Polyphenylene Ester (PPE)
<b>Surface resistance</b>	$\geq 1 \times 10^4$ to $< 1 \times 10^{11}$ ohms
<b>Bakability</b>	Tray can be baked at max. 125 °C for 24 h. Please remove fitment before baking if attached.
<b>Tray capacity</b>	14 converters/tray
<b>Box capacity</b>	42 products (3 full trays/box)
<b>Tray weight</b>	215 g empty tray, 1800g full tray.

JEDEC standard tray.  
All dimensions in mm

Tolerances: X.x  $\pm 0.26$  [0.01], X.xx  $\pm 0.13$  [0.005]

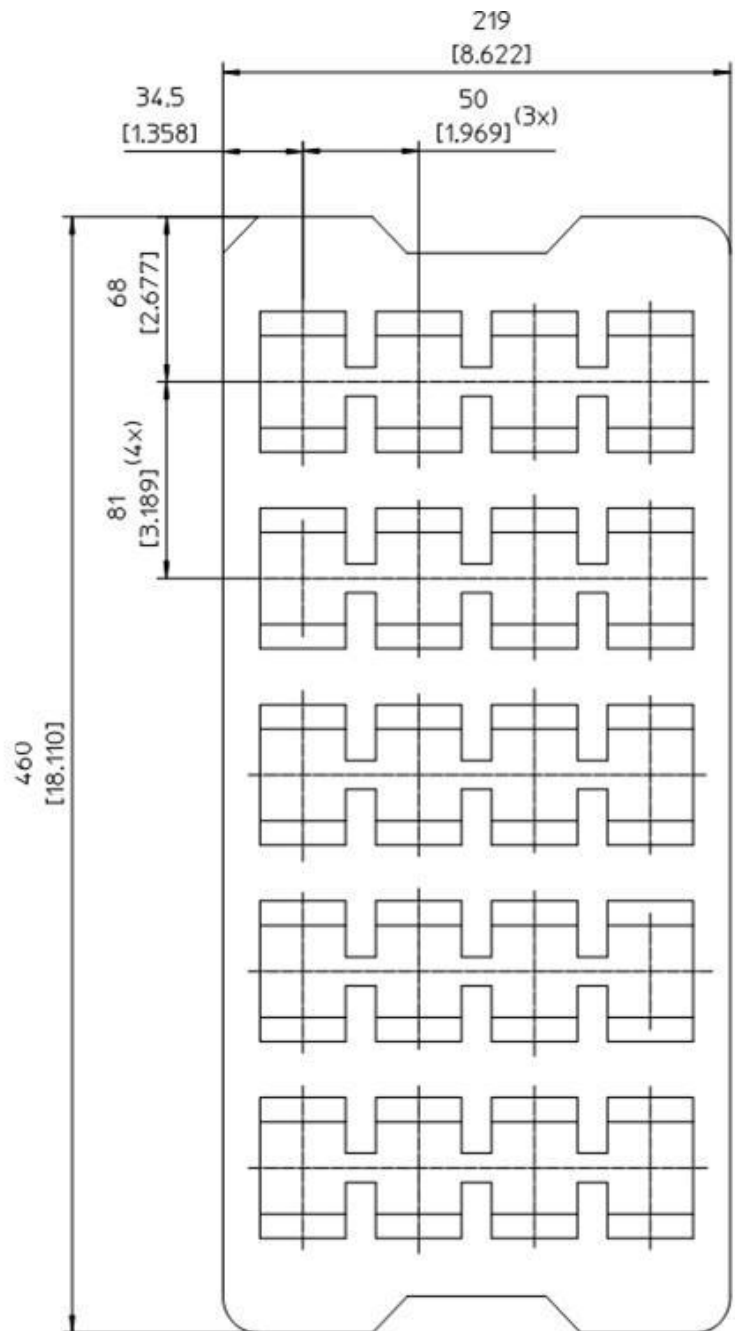


Example hard tray (14 pcs)

**Packaging information**

**Blank option:** Select for wave or hand soldering, NOT dry packed.

<b>Material</b>	Antistatic Polyethylene (PE) foam
<b>Surface resistance</b>	$\geq 1 \times 10^4$ to $< 1 \times 10^{11}$ ohms
<b>Bakability</b>	Tray cannot be baked
<b>Tray capacity</b>	20 converters / tray
<b>Box capacity</b>	60 products (3 full trays/ box)
<b>Weight</b>	48 g empty tray, 2248 g full tray.



Example PE foam tray

## Part 6: Revision history

### Revision table

Revision number	revision change	date	revisor
Rev. A	New TS document	2024-11-15	jjdjaja
Rev. B	Minor formatting updates	2024-11-15	kartwaer
Rev. C	Add 20MHz bandwidth for enable signal measurement	2025-07-16	jjdmawan
Rev. D	Minor formatting updates	2026-02-02	kartwaer

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